1. Which logic gates are used to construct the S-R Latch
NAND
2. Sequential circuits must contain elements
Memory
3. How many inputs does a T flip-flop have
One
4. Which logic gate do you add to convert an "S-R Latch" to an "S-R Latch with Enable"
NOT
5. What does the output of sequential circuits depend on
Current and past inputs
6. What state is a "J-K Flip-Flop" in when $J = K = 1$
Toggle state
7. Which logic gate do you add to convert an "S-R Latch" to a "D Latch"
NOT

8. What is the output of a 3-input NOR gate with inputs 1, 0, 1  $\,$ 

0

9. What is the output of a 3-input 'AND' gate with inputs - 1, 0, 0

0

10. Which logic gates are used to construct the Bistable Element

NAND